	Time Stamp	Comments	Error Definition	Errors
1	2002/05/16 14:41		-	0
2	2002/05/16 15:36			0
3	2002/05/16 14:44			0
4	2002/05/16 15:36			0
5	2002/05/16 15:03			0
6	2002/05/16 15:09			0
7	2002/05/16 15:07			0
8	2002/05/16 16:11			0
9	2002/05/16 16:05		-	0
10	2002/05/16 16:05			0
11	2002/05/16 15:41			0
12	2002/05/16 15:43			0
13	2002/05/16 16:05			0
14	2002/05/16 16:19			0
15	2002/05/16 16:25			0
16	2002/05/16 16:23			0
17	2002/05/16 16:25			0

	Туре	Hits	Search Text	DBs
18	BRS	55	left) adj3 (register or latch or buffer or FIFO)))	USPAT; EPO; JPO; DERWENT; IBM_TDB
19	BRS	1038	((part or first or portion) adj data) same ((first or left) adj (buffer or FIFO or register or latch))	USPAT; EPO; JPO; DERWENT; IBM_TDB
20	IS&R	495	(711/103).CCLS.	USPAT; EPO; JPO; DERWENT; IBM_TDB
21	BRS	2		USPAT; EPO; JPO; DERWENT; IBM_TDB
22	BRS	3	(flash adj memory)	USPAT; EPO; JPO; DERWENT; IBM_TDB
23	BRS	57	<pre>((part or first or portion) adj data) same ((first or left) adj (buffer or FIFO or register or latch)) same (memory adj cell)</pre>	

	Time Stamp	Comments	Error Definition	Errors
	2002/05/16 16:30			0
19	2002/05/17 09:41			0
20	2002/05/17 09:14			0
	2002/05/17 09:14		•	0
22	2002/05/17 09:59			0
	2002/05/17 10:03			0

	Туре	Hits	Search Text	DBs
1	IS&R	495	(711/103).CCLS.	USPAT; EPO; JPO; DERWENT; IBM_TDB
2	BRS	385	(first adj data) same (first adj register)	USPAT; EPO; JPO; DERWENT; IBM_TDB
3	BRS	1	((711/103).CCLS.) and ((first adj data) same (first adj register))	USPAT; EPO; JPO; DERWENT; IBM_TDB
4	BRS	657	((part or first) adj data) same (first adj (register or latch))	USPAT; EPO; JPO; DERWENT; IBM_TDB
5	BRS	1	((711/103).CCLS.) and (((part or first) adj data) same (first adj (register or latch)))	USPAT; EPO; JPO; DERWENT; IBM_TDB
6	BRS	1089	(word adj line) same (first adj bit adj line) same (first memory adj cell)	USPAT; EPO; JPO; DERWENT; IBM_TDB
7	BRS	0	((711/103).CCLS.) and ((word adj line) same (first adj bit adj line) same (first memory adj cell))	USPAT; EPO; JPO; DERWENT; IBM_TDB
8	BRS	113	<pre>((non-volatile) adj memory) and ((word adj line) same (first adj bit adj line) same (first memory adj cell))</pre>	USPAT; EPO; JPO; DERWENT; IBM_TDB
9	BRS	10	<pre>(activat\$3 adj word adj line) and (((non-volatile) adj memory) and ((word adj line) same (first adj bit adj line) same (first memory adj cell)))</pre>	USPAT; EPO; JPO; DERWENT; IBM_TDB
10	BRS	415	((part or first) adj data) near10 ((first or left) adj (register or latch))	USPAT; EPO; JPO; DERWENT; IBM_TDB
11	BRS	0	((711/103).CCLS.) and (((part or first) adj data) near10 ((first or left) adj (register or latch)))	USPAT; EPO; JPO; DERWENT; IBM_TDB
12	BRS	15	<pre>((non-volatile) adj memory) and (((part or first) adj data) near10 ((first or left) adj (register or latch)))</pre>	USPAT; EPO; JPO; DERWENT; IBM_TDB
13	BRS	0 .	<pre>((activat\$3 adj word adj line) and (((non-volatile) adj memory) and ((word adj line) same (first adj bit adj line) same (first memory adj cell)))) and (((part or first) adj data) near10 ((first or left) adj (register or latch)))</pre>	USPAT; EPO; JPO; DERWENT; IBM_TDB
14	BRS	639	((part or first or portion) adj data) near10 ((first or left) adj (register or latch or buffer or FIFO))	USPAT; EPO; JPO; DERWENT; IBM_TDB
15	BRS	25	<pre>((non-volatile) adj memory) and (((part or first or portion) adj data) near10 ((first or left) adj (register or latch or buffer or FIFO)))</pre>	USPAT; EPO; JPO; DERWENT; IBM_TDB
16	BRS	1464	((part or first or portion or section) adj data) near10 ((first or left) adj3 (register or latch or buffer or FIFO))	USPAT; EPO; JPO; DERWENT; IBM_TDB
17	BRS	5	((711/103).CCLS.) and (((part or first or portion or section) adj data) near10 ((first or left) adj3 (register or latch or buffer or FIFO)))	USPAT; EPO; JPO; DERWENT; IBM_TDB

	Туре	Hits	Search Text	DBs
1	IS&R	495	(711/103).CCLS.	USPAT; EPO; JPO; DERWENT; IBM_TDB
2	BRS	385	(first adj data) same (first adj register)	USPAT; EPO; JPO; DERWENT; IBM_TDB
3	BRS	1	((711/103).CCLS.) and ((first adj data) same (first adj register))	USPAT; EPO; JPO; DERWENT; IBM_TDB
4	BRS	657	((part or first) adj data) same (first adj (register or latch))	USPAT; EPO; JPO; DERWENT; IBM_TDB
5	BRS	1	((711/103).CCLS.) and (((part or first) adj data) same (first adj (register or latch)))	USPAT; EPO; JPO; DERWENT; IBM_TDB
6	BRS	1089	(word adj line) same (first adj bit adj line) same (first memory adj cell)	USPAT; EPO; JPO; DERWENT; IBM_TDB
7	BRS	0	((711/103).CCLS.) and ((word adj line) same (first adj bit adj line) same (first memory adj cell))	USPAT; EPO; JPO; DERWENT; IBM_TDB
8	BRS	113	<pre>((non-volatile) adj memory) and ((word adj line) same (first adj bit adj line) same (first memory adj cell))</pre>	USPAT; EPO; JPO; DERWENT; IBM_TDB
9	BRS	10	<pre>(activat\$3 adj word adj line) and (((non-volatile) adj memory) and ((word adj line) same (first adj bit adj line) same (first memory adj cell)))</pre>	USPAT; EPO; JPO; DERWENT; IBM_TDB
10	BRS	415	((part or first) adj data) near10 ((first or left) adj (register or latch))	USPAT; EPO; JPO; DERWENT; IBM_TDB
11	BRS	0	((711/103).CCLS.) and (((part or first) adj data) near10 ((first or left) adj (register or latch)))	USPAT; EPO; JPO; DERWENT; IBM_TDB
12	BRS	15	<pre>((non-volatile) adj memory) and (((part or first) adj data) near10 ((first or left) adj (register or latch)))</pre>	USPAT; EPO; JPO; DERWENT; IBM_TDB
13	BRS	0	<pre>((activat\$3 adj word adj line) and (((non-volatile) adj memory) and ((word adj line) same (first adj bit adj line) same (first memory adj cell)))) and (((part or first) adj data) near10 ((first or left) adj (register or latch)))</pre>	USPAT; EPO; JPO; DERWENT; IBM_TDB
14	BRS	639	((part or first or portion) adj data) near10 ((first or left) adj (register or latch or buffer or FIFO))	USPAT; EPO; JPO; DERWENT; IBM_TDB
15	BRS	25	<pre>((non-volatile) adj memory) and (((part or first or portion) adj data) near10 ((first or left) adj (register or latch or buffer or FIFO)))</pre>	USPAT; EPO; JPO; DERWENT; IBM_TDB
16	BRS	1464	((part or first or portion or section) adj data) near10 ((first or left) adj3 (register or latch or buffer or FIFO))	USPAT; EPO; JPO; DERWENT; IBM_TDB
17	BRS	5	((711/103).CCLS.) and (((part or first or portion or section) adj data) near10 ((first or left) adj3 (register or latch or buffer or FIFO)))	USPAT; EPO; JPO; DERWENT; IBM_TDB

	Time Stamp	Comments	Error Definition	Errors
1	2002/05/16 14:41			0
2	2002/05/16 15:36			0
3	2002/05/16 14:44			0
4	2002/05/16 15:36			0
5	2002/05/16 15:03			0
6	2002/05/16 15:09			0
7	2002/05/16 15:07			0
8	2002/05/16 16:11			0
9	2002/05/16 16:05			0
10	2002/05/16 16:05			0
11	2002/05/16 15:41			0
12	2002/05/16 15:43			0
13	2002/05/16 16:05			0
14	2002/05/16 16:19			0
15	2002/05/16 16:25			0
16	2002/05/16 16:23			0
17	2002/05/16 16:25			0

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	Туре	Hits	Search Text	DBs
18	BRS	55	<pre>((non-volatile) adj memory) and (((part or first or portion or section) adj data) near10 ((first or left) adj3 (register or latch or buffer or FIFO)))</pre>	USPAT; EPO; JPO; DERWENT; IBM_TDB
19	BRS	1038	((part or first or portion) adj data) same ((first or left) adj (buffer or FIFO or register or latch))	USPAT; EPO; JPO; DERWENT; IBM_TDB
20	IS&R	495	(711/103).CCLS.	USPAT; EPO; JPO; DERWENT; IBM TDB
21	BRS	2	(((part or first or portion) adj data) same ((first or left) adj (buffer or FIFO or register or latch))) and ((711/103).CCLS.)	USPAT; EPO; JPO; DERWENT; IBM_TDB
22	BRS	3	((part or first or portion) adj data) same ((first or left) adj (buffer or FIFO or register or latch)) same (flash adj memory)	USPAT; EPO; JPO; DERWENT; IBM_TDB
23	BRS	57	((part or first or portion) adj data) same ((first or left) adj (buffer or FIFO or register or latch)) same (memory adj cell)	USPAT; EPO; JPO; DERWENT; IBM_TDB
24	BRS	667	(memory adj cell) with (first adj data) with (second adj data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
25	BRS	169	(memory adj cell) with (first adj data) with (second adj data) with writ\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
26	BRS	44	(first adj (register or latch or buffer)) with (first adj data) with (memory adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
27	BRS	22	(first adj (register or latch or buffer)) with (first adj data) with (memory adj cell) with (second adj data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
28	BRS	181	pipeline adj read	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
29	BRS	0	read with ouput with (read adj again)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
30	BRS	0	read with ouput and (pipeline adj read)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
31	BRS	443	overlap\$3 with output with read\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
32	BRS	0	overlap\$3 with output with then with read\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
33	BRS	443	overlap\$3 with output with read\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
34	BRS	78	overlap\$3 near5 output near5 read\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
3.5	BRS	92	(pipeline adj read) and output near5 read\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB

	Time Stamp	Comments	Error Definition	Errors
18	2002/05/16 16:30			0
19	2002/05/17 09:41	<u>i</u>		0
20	2002/05/17 09:14			0
21	2002/05/17 09:14			o
22	2002/05/17 09:59			0
23	2002/05/17 10:03			0
24	2002/11/14 17:11			0
25	2002/11/14 17:12			0
26	2002/11/14 17:13			0
27	2002/11/14 17:16			0
28	2002/11/15 10:36			0
29	2002/11/15 10:17			0
30	2002/11/15 10:17			0
31	2002/11/15 10:21			0
32	2002/11/15 10:22			0
33	2002/11/15 10:24			0
34	2002/11/15 10:36			0
35	2002/11/15 10:36			0



	Туре	Hits	Search Text	DBs
36	BRS	0	(pipeline adj read) and (overlap\$3 near5 output near5 read\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
37	BRS	0	(pipeline adj read) same (output with overlap\$4 with read\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
38	BRS	8	(pipeline) same (output with overlap\$4 with read\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
39	BRS	54	(pipeline or parallel) same (output with overlap\$4 with read\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT, IBM TDB
40	BRS	120	output\$4 near5 (first near5 data) near10 read\$3 near5 (overlap\$4 or pipeline or parallel)	USPAT; US-PGPUB; EPO; JPO; DERWENT, IBM TDB
41	BRS	7	output\$4 near5 (first adj data) near10 read\$3 near5 (overlap\$4 or pipeline or parallel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
42	BRS	2445	(output\$4 near5 data) with (read\$3 near5 data) with (overlap\$4 or pipeline or parallel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
43	BRS	0	(output\$4 adj data) with (read\$3 adj data) with (overlap\$4 or pipeline or parallel) with wordline	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
44	BRS	158		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
45	BRS	4683	pipeline near10 (reason or advantage)	USPAT; EPO; JPO; DERWENT; IBM TDB
46	BRS	251	(write or store) with ("same" adj memory adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
47	BRS	128	(write or store) near5 ("same" adj memory adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
48	BRS	3	(write or store) near5 ("same" adj memory adj cell) near10 overlap\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
49	BRS	4	(write or store) with ("same" adj memory adj cell) with overlap\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
50	BRS	0	(write or store) near5 (first adj data) near10 ("same" adj memory adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB
51	BRS	2	:	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
52	BRS	2	second) near5 data) near10 ("same" adj memory adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
53	BRS	16		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB

	Time Stamp	Comments	Error Definition	Errors
36	2002/11/15 10:36			0
37	2002/11/15 10:37			0
38	2002/11/15 10:43			0
39	2002/11/15 10:54			0
40	2002/11/15 11:00			0
41	2002/11/15 11:09			0
42	2002/11/15 11:17			0
43	2002/11/15 11:23		- The state of the	0
44	2002/11/15 11:27			0
45	2002/11/15 13:06			0
46	2002/11/15 14:06			0
47	2002/11/15 14:09			0
48	2002/11/15 13:59			0
49	2002/11/15 14:07			0
50	2002/11/15 14:11			0
51	2002/11/15 14:12			0
52	2002/11/15 14:16			0
53	2002/11/15 14:17			0

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